CM Vijay releases Tamil Nadu Semiconductor Ecosystem Scheme – State incentives on top of 'Semicon India 2.0'

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News

CM C. Joseph Vijay released the 'Tamil Nadu Semiconductor Ecosystem Scheme' (policy) at the Secretariat, Chennai, on 15 Oct 2026. It covers the whole value chain – chip design, equipment and materials, wafer fabs, assembly-testing-marking-packaging (ATMP/OSAT) and R&D. Projects approved under the Centre's 'Semicon India 2.0' will get State incentives on top of central support; other key projects will get tailored support. The incentive percentages have not yet been published.

Key facts

Release	15 Oct 2026 – CM C. Joseph Vijay, Secretariat, Chennai; Industries, Investment Promotion & Commerce Department
Coverage	Chip design, equipment & materials, wafer fabs, ATMP/OSAT, R&D
Incentives	State top-up over central support for Semicon India 2.0 projects; tailored support for others
Semicon 2.0	Union Cabinet approval 15 July 2026; ₹1,27,500 crore
State park	SIPCOT semiconductor & electronics park at Maduramangalam, Kancheepuram – ₹175 crore (Rule 110 announcement, 31 Aug 2026)

Background

- India Semiconductor Mission (ISM) / Semicon India 1.0 – Dec 2021, ₹76,000 crore; 12 projects in 6 States. Tata–PSMC wafer fab – Dholera (Gujarat); Micron ATMP – Sanand (Gujarat).
- Science: semiconductors conduct between conductors and insulators – silicon, germanium; doping creates n-type (phosphorus) and p-type (boron) material.
- Earlier State effort: 'Tamil Nadu Semiconductor Mission 2030' (2025–26 Budget, ₹500 crore) – two semiconductor manufacturing parks at Sulur (Coimbatore) and Palladam (Tiruppur).

Tamil Nadu link: Tamil Nadu has led India's electronics exports for three years running – $14.65 billion in 2024–25, 41.23% of the national total.

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